REMARKS

Reconsideration of this application is respectfully requested in view of the foregoing amendment and the following remarks.

Claims 1-11, which were pending in this application, have been cancelled. New claims 12-30 have been added. Support for new independent claim 12 can be found, for example, in paragraphs [0027] and [0028] of the published specification dated April 7, 2005 (hereinafter the published specification) with respect to Figs. 4 and 5. Support for the new independent claim 19 can be found, for example, in Table 2 and paragraphs [0028] to [0030] of the published specification with respect to Fig. 6. Support for new independent claim 26 can be found, for example, in Table 2 and paragraphs [0031] to [0035] of the published specification with respect to Figs. 7a to 7c and 8a to 8c. Applicants respectfully submit that no new matter has been added by the addition of new claims 12-30.

Priority Document

Applicants have submitted the priority document.

Claims Objections

Claim 10 was objected to because of typographic informalities. Since Claim 10 has been cancelled, the reason for the objection of Claim 10 has become moot.

Claims Rejections under 35 U.S.C. § 112

Claim 5 was rejected under 35 U.S.C. § 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which Applicants regard as the invention. Since Claim 5 has been cancelled, the reason for the rejection of Claim 5 has become moot.

Claims Rejections under 35 U.S.C. § 102 (b)

Claims 1-11 have been rejected under 35 U.S.C. § 102 (b) as allegedly being anticipated by Chartier (U.S. Patent No. 4,938,567, hereinafter the '567 patent.) Since Claims 1-11 have been cancelled, the reasons for the rejection of Claims 1-11 have become moot. However, for the reasons set forth below, new claims 12-30 distinguish patentability over the '567 patent.

The '567 patent relates to "an electro-optical panel and, more especially, an intersection point transistor structure made with thin films, wherein there is provided a doubling of the line and column electrodes (LG, CL) by doubling elements ($1g_1$, Col. 1, Col. 2), as well as a light barrier (EC) shielding a transistor." (Abstract.) "These advantages are redundancy in lines and columns which are doubled to avoid breaks, double insulation at the intersection of the lines and columns with a silicon pad, if necessary, as a reinforcement." (Column 2, lines 29-32) (Emphasis added.)

Unlike the object of providing redundancy, an object of the present application is to provide "a wire structure and a manufacturing method thereof, by using an at least two-layered wire structure to reduce the resistance of the wire." (Paragraph [0006], lines 1-4 of the published specification) (Emphasis added.) Table 2 of the published specification shows the relationship between a ratio of the length of the branch line 202 to the width of the fillister 206 and an overall resistance. Furthermore, it is indicated that "in the double-layered, tooth-like wire structure of the present invention, when the proportion of the branch line 202 parallel to the main line 200 increases, i.e. the ratio b/a increases, the resistance of the overall wire decreases." (Paragraph [0028], lines 7-10) (Emphasis added.) Such a feature is not described or illustrated in the '567 patent.

Applicants therefore have added new independent claim 12 to recite that "one of a first ratio of the first length to the first distance or a second ratio of the second length to the second distance is predetermined in order to reduce the resistance of the multi-layered complementary wire structure." (Emphasis added.) Moreover, new claims 13-16 have been added to recite the range of the first ratio and second ratio. Support for the new claims 13-16 can be found, for example, in Table 2 of the published specification.

Applicants have added new independent Claim 19 to recite that "one of a first ratio of the first length to the first distance or a second ratio of the second length to the second distance is predetermined in order to reduce the resistance of the matrix structure of the display." (Emphasis added.) Applicants have also added new independent claim 26 to recite that "one of a first ratio of the first length to the first distance or a second ratio of the second length to the second distance is predetermined in order to reduce the resistance of the multi-layered complementary wire structure." (Emphasis added.)

The pending independent Claims 12, 19 and 26 are distinguishable from the '567 patent. Claims 13-18, 20-25, and 27-30 depending from Claims 12, 19 and 26, respectively, are therefore also distinguishable from the '567 patent.

In view of the foregoing, all of the claims are believed to be in condition for allowance. Should the Examiner have any questions or determine that any further action is desirable to place this application in even better condition for issue, the Examiner is encouraged to telephone Applicants' undersigned representative at the number listed below.

Respectfully submitted,

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(Date)

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